

## 6

## Digital Design Project #6: Circuit delays and glitches

Submission form revision September 5, 2025

RealDigital  
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I am submitting my own work, and I accept that penalties will be assessed against me if I submit work that isn't mine.

## Point Scale

4: Exemplary  
 3: Complete  
 2: Incomplete  
 1: Minor effort  
 0: Not submitted

Print Name

Sign Name

Date

Asst. Signature

## Circle one column below and modify earned scores accordingly

Weeks Early / Late	-3	-2	-1	0	1	2	3	4	5+
Requirements Points	+9	+6	+3	0	-3	-6	-9	-12	-15
Challenges Points	+4	+3	+2	0	-1	-2	-3	-4	-5

Requirements	Wt	Pt	Score	Possible	Earned
1. Simulate a glitch	2			32	
2. Change OR gate delay	2				
3. Change all gate delays	2				
4. Homework problems	2				
				Modify earned scores by submission week according to table above.	
Challenges				Possible	Earned
1. Decoder delay	3			12	