

# PROJECT 4: COMBINATIONAL BUILDING BLOCKS

## Digital Logic Project Submission Form



Revision: 9/3/24

I am submitting my own work, and I accept that penalties will be assessed against me if I submit work that isn't mine.

**Point Scale**  
4: Exemplary  
3: Complete  
2: Incomplete  
1: Minor effort  
0: Not submitted

Print Name

Sign Name

Date

#

*Deliverable*

*Wt*

*Pts*

*Date*

*Asst. Signature*

### Requirements

#### 1: Multiplexor

Program demo

2

Verbal questions answered well

2

#### 2: Multiplexor (bus)

Program demo

2

Verbal questions answered well

2

#### 3: Decoder

Program demo

3

Verbal questions answered well

3

#### 4: Encoder

Program demo

3

Verbal questions answered well

3

#### 5: Shifter

Program demo

4

Verbal questions answered well

4

### Challenges

#### 1: 12:1 Multiplexor Circuit

Program demo

2

Verbal questions answered well

2

#### 2: Minterm Multiplexor Circuit

Program demo

2

Verbal questions answered well

2

### Extension

Describe

Program demo

Verbal questions answered well