

# PROJECT 4: COMBINATIONAL BUILDING BLOCKS

## Digital Logic Project Submission Form

Revision: 9/3/24

I am submitting my own work, and I accept that penalties will be assessed against me if I submit work that isn't mine.

**Point Scale**  
 4: Exemplary  
 3: Complete  
 2: Incomplete  
 1: Minor effort  
 0: Not submitted

 \_\_\_\_\_  
 Print Name

 \_\_\_\_\_  
 Sign Name

 \_\_\_\_\_  
 Date

#	Deliverable	Wt	Pts	Date	Asst. Signature
<b>Requirements</b>					
<b>1: Multiplexor</b>					
	Program demo	2			
	Verbal questions answered well	2			
<b>2: Multiplexor (bus)</b>					
	Program demo	2			
	Verbal questions answered well	2			
<b>3: Decoder</b>					
	Program demo	3			
	Verbal questions answered well	3			
<b>4: Encoder</b>					
	Program demo	3			
	Verbal questions answered well	3			
<b>5: Shifter</b>					
	Program demo	4			
	Verbal questions answered well	4			
<b>Challenges</b>					
<b>1: 12:1 Multiplexor Circuit</b>					
	Program demo	2			
	Verbal questions answered well	2			
<b>2: Minterm Multiplexor Circuit</b>					
	Program demo	2			
	Verbal questions answered well	2			
<b>Extension</b>					
<b>Describe</b>					
	Program demo				
	Verbal questions answered well				