Digital Structural Verilog Logic Submission form revision Decem





| I am submitting my own work, and I accept penalties will be assessed against me if I submit work that isn't mine. Print Name Sign Name Date Asst. Signature | | | | | | Point Scale 4: Exemplary 3: Complete 2: Incomplete 1: Minor effort 0: Not submitted | |
|--|---------|---|--|---|-----|---|-------|
| Requirements | | | | Ü | Pts | x Wt = | Score |
| 1. Mux/Demux x 3 = | | | | | | | |
| 2. Seven-segment decoder x 3 | | | | | | x 3 = | |
| 3. Two-digit seven-segment decoder | | | | | | x 4 = | |
| 4. Homework problems | | | | | | x 2 = | |
| Total Score (add all rows) | | | | | | | |
| Leave blank for on-time submission, or: Enter $+5$ if one or more weeks early; Enter -12 if one or two weeks late; Enter X if two or more weeks late | | | | | | | |
| Requirements Possible Score | 48 | Add the two rows above for the Requirements Earned Score Enter $m{	heta}$ if more than two weeks late | | | | | |
| Challenges Pts x Wt | | | | | | | Score |
| 1. Two-digit seven-segment decoder using a counter x 3 | | | | | | x 3 = | |
| Total Score (add all rows) | | | | | | | |
| Leave blank for on-time submission, or Enter $+2$ if one or more weeks early; Enter -4 if one or two weeks late; Enter X if two or more weeks late | | | | | | | |
| Challenges Possible Score | · / | | | | | | |