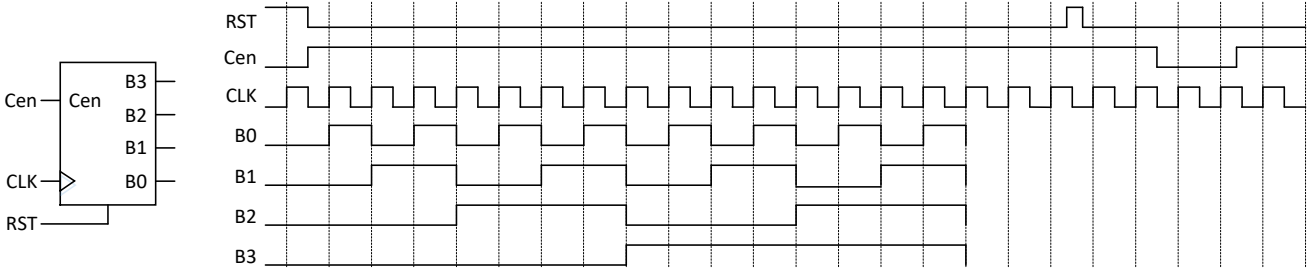


Problem Set #8

1. (12 points) Complete the timing diagram below to illustrate the behavior of the counter.



2. (12 points) Complete the circuit sketch for a four-bit counter by adding the required next-state logic gates in front of the flip-flops.

