

PROJECT 8: COUNTERS, CLOCK DIVIDERS, AND THE 7-SEGMENT DISPLAY

Digital Logic Project Submission Form

Revision Date: Sept 28, 2018



I am submitting my own work, and I accept that penalties will be assessed against me if I submit work that isn't mine.

Point Scale
 4: Exemplary
 3: Complete
 2: Incomplete
 1: Minor effort
 0: Not submitted

Print Name

Sign Name

Date

#	Deliverable	Wt	Pts	Date	Asst. Signature
Requirements					
1: Simulate Cascade Clock Divider and Test it on the Blackboard					
1	Program demo	1			
2	Verbal questions answered well	1			
2: Counter Based Clock					
1	Program demo	2			
2	Verbal questions answered well	2			
3: Counter Based Clock Divider					
1	Program demo	2			
2	Verbal questions answered well	2			
4: BCD Counter					
1	Program demo	3			
2	Verbal questions answered well	3			
5: 4-digit BCD Counter					
1	Program demo	3			
2	Verbal questions answered well	3			
Challenges					
1: 4 Digit Counter Controller					
1	Program demo	1			
2	Verbal questions answered well	1			
Extensions					
Describe					
1	Program demo				
2	Verbal questions answered well				
Homework Problems					
1	Counter Timing Diagram	12			
2	4-Bit Counter	12			