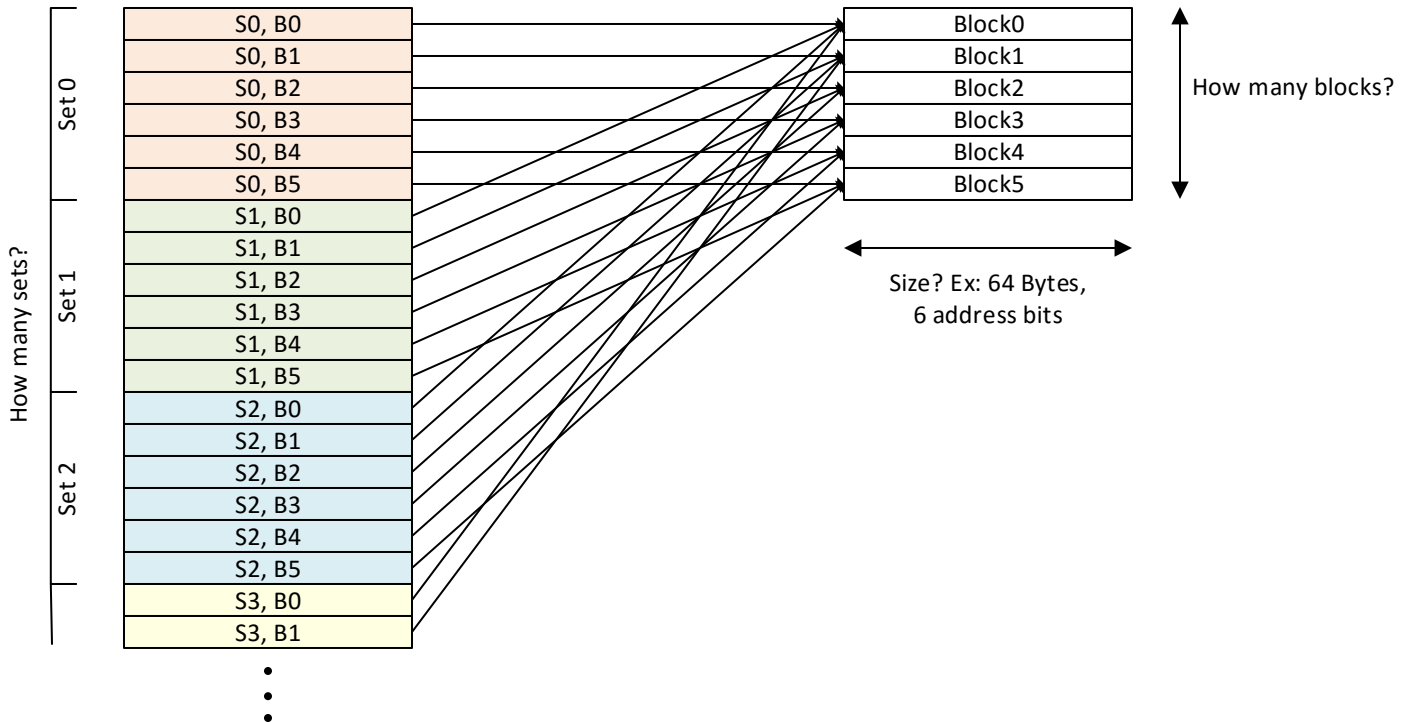


Direct-mapped cache – each main memory location can only go to one cache location



When to move a “cache line” from MM to cache?

For a read, how to tell when cache has needed data?

Tag. If Tag matches entry in “cache table”, then data is in cache

How big is “cache table”? Each block needs a tag

Where are tags stored? Main memory?

If CPU writes data back to cache, when to write that data to MM?

If cache block needs to be overwritten with new cache line... write back?

Set associativity...

Associative cache – each main memory location can go to one of several cache locations

