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Digital Design Project #5: Structural Verilog

Submission form revision September 5, 2025

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I am submitting my own work, and I accept that penalties will be assessed against me if I submit work that isn't mine.

Point Scale

4: Exemplary
3: Complete
2: Incomplete
1: Minor effort
0: Not submitted

Print Name

Sign Name

Date

Asst. Signature

Circle one column below and modify earned scores accordingly

Weeks Early / Late	-3	-2	-1	0	1	2	3	4	5+
Requirements Points	+12	+8	+4	0	-4	-8	-12	-16	-20
Challenges Points	+3	+2	+1	0	-1	-2	-3	-4	-5

Requirements	Wt	Pt	Score	Possible	Earned	
1. Mux/Demux	3			48		
2. Seven-segment decoder	3					
3. Two-digit seven-segment decoder	4					
4. Homework problems	2					
				Modify earned scores by submission week according to table above.		
Challenges				Possible	Earned	
1. Two-digit seven-segment decoder using a counter	3			12		