PROJECT 4: COMBINATIONAL BUILDING BLOCKS

Digital Logic Project Submission Form   Revision Date: Sept 28, 2018   **Real**Digital

I am submitting my own work, and I accept that penalties will be assessed against me if I submit work that isn't mine.

Print Name	Sign Name	Date

#	Deliverable	Wt	Pts	Date	Asst. Signature
Requirements					
1: Multiplexor					
1	Program demo	2			
2	Verbal questions answered well	2			
2: Decoder					
1	Program demo	3			
2	Verbal questions answered well	3			
3: Encoder					
1	Program demo	3			
2	Verbal questions answered well	3			
4: Shifter					
1	Program demo	4			
2	Verbal questions answered well	4			
Challenges					
1: Minterm Multiplexor Circuit					
1	Program demo	2			
2	Verbal questions answered well	2			
Extensions					
Describe					
1	Program demo				
2	Verbal questions answered well				
Homework Problems					
1	Mux circuit sketch	10			
2	Mux circuit from minterm equation	10			
3	Mux circuit from K-map	8			
4	Larger mux from smaller muxes	6			
5	Decoder circuit sketch	10			
6	Decoder circuit fro minterm equation	10			
7	Larger decoder from smaller	4			
8	Decoder circuit from K-map	10			
9	Mux/Demux circuit	15			
10	Shifter	12			