

# Digital Systems Project #4: PWM IP Block

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## Point Scale

4: Exemplary  
3: Complete  
2: Incomplete  
1: Minor effort  
0: Not submitted

Print Name

Sign Name

Date

Asst. Signature

## Requirements

Wt

Pt

Score

Possible

Earned

1. Design a hardware PWM block in Verilog

6

2. Make the PWM IP an AXI peripheral to control RGB LEDs

7

3. Make it a full-featured PWM block

6

76

Weeks late: 1 2 3 4+  
(Circle as indicated)

## Challenges

Possible

Earned

1. Create a hardware PWM block to control servo motors

7

2. Generate simple waveforms using a PWM module

7

56