PROJECT 1: INTRODUCTION TO THE BLACKBOARD, VIVADO, AND VERILOG

Digital Logic Project Submission Form Revision Date: Sept 28, 2018   **Real**Digital

I am submitting my own work, and I accept that penalties will be assessed against me if I submit work that isn't mine.	Point Scale
	4: Exemplary
	3: Complete
	2: Incomplete
	1: Minor effort
Print Name Sign Name Date	0: Not submitted

#	Deliverable	Wt	Pts	Date	Asst. Signature
Requirements					
1: Verify the Demo Project					
1	Program demo	1			
2	Verbal questions answered well	1			
2: Verify the Second Demo Project					
1	Program demo	2			
2	Verbal questions answered well	2			
3: Try using All LEDs and Switches					
1	Program demo	2			
2	Verbal questions answered well	2			
4: Reverse the Order					
1	Program demo	3			
2	Verbal questions answered well	3			
5: Illuminate the Seven-Segment Display					
1	Program demo	3			
2	Verbal questions answered well	3			
Challenges					
1: Drive RGB LED Using Pushbuttons					
1	Program demo	1			
2	Verbal questions answered well	1			
2: Include Master ON/OFF Switch					
1	Program demo	1			
2	Verbal questions answered well	1			
Extensions					
Describe					
1	Program demo				
2	Verbal questions answered well				
Homework Problems					
1	Simple Digital System	15			
2	AND OR Relationship	22			
3	pFET and nFET	4			
4	Truth Tables for Logic Gates	20			
5	Truth Table for a Logic Gate	10			
6	3-Input NAND and 4-input NOR	12			
7	CAD Design of a Logic Gate	8			