

# Digital Systems Project #3: AXI Bus

Submission form revision August 8, 2025

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## Point Scale

4: Exemplary  
3: Complete  
2: Incomplete  
1: Minor effort  
0: Not submitted

Print Name

Sign Name

Date

Asst. Signature

## Requirements

Wt

Pt

Score

Possible

Earned

1. Demonstrate AXI-4 Read/Write transaction

2

2. Create AXI-4 lite logic

2

3. Simulate AXI4 write transaction

3

4. Simulate AXI4 read transaction

3

5. Create a new IP block for LEDs

3

52

Weeks late: 1 2 3 4+  
(Circle as indicated)

## Challenges

Possible

Earned

1. Use the ILA to verify hardware performance

5

20