

# PROJECT 3: INTRODUCTION TO AXI4-LITE

## Digital Systems Project Submission Form

Revision: 9/3/19

I am submitting my own work, and I accept that penalties will be assessed against me if I submit work that isn't mine.

### Point Scale

- 4: Exemplary
- 3: Complete
- 2: Incomplete
- 1: Minor effort
- 0: Not submitted

Print Name

Sign Name

Date

#	Deliverable	Wt	Pts	Date	Asst. Signature
<b>Requirements</b>					
<b>1: Demonstrate the AXI4-Lite Read/Write transaction at Runtime</b>					
	Program demo	2			
	Verbal questions answered well	2			
<b>2: Create your own AXI4_Lite interface block</b>					
	Program demo	4			
	Verbal questions answered well	4			
<b>3: Simulate the myled IP block AXI interface write transaction</b>					
	Program demo	3			
	Verbal questions answered well	3			
<b>4: Simulate the myled IP block AXI interface read transaction</b>					
	Program demo	3			
	Verbal questions answered well	3			
<b>5: Create a new IP block to read the switches and drive the LEDs</b>					
	Program demo	3			
	Verbal questions answered well	3			
<b>Challenges</b>					
<b>1: Use the hardware debugger (logic analyzer) to prove your new IP block works correctly</b>					
	Program demo	3			
	Verbal questions answered well	3			
<b>Extensions</b>					
<b>Describe</b>					
	Program demo				
	Verbal questions answered well				
<b>Homework Problems</b>					
1	Input processing state diagram	16			
2	Mealy question	4			