

# PROJECT 9: COMPARATORS, ADDERS, AND MULTIPLIERS

Digital Logic Project Submission Form

Revision Date: Sept 28, 2018



I am submitting my own work, and I accept that penalties will be assessed against me if I submit work that isn't mine.

Point Scale  
 4: Exemplary  
 3: Complete  
 2: Incomplete  
 1: Minor effort  
 0: Not submitted

Print Name

Sign Name

Date

#	Deliverable	Wt	Pts	Date	Asst. Signature
<b>Requirements</b>					
<b>1: 4-bit CLA</b>					
1	Program demo	2			
2	Verbal questions answered well	2			
<b>2: Adder/Subtractor</b>					
1	Program demo	2			
2	Verbal questions answered well	2			
<b>3: Behavioral Multiplier</b>					
1	Program demo	3			
2	Verbal questions answered well	3			
<b>4: Comparator</b>					
1	Program demo	4			
2	Verbal questions answered well	4			
<b>Extensions</b>					
<b>Describe</b>					
1	Program demo				
2	Verbal questions answered well				
<b>Homework Problems</b>					
1	Magnitude Comparator Bit Slice Circuit	12			
2	Modify Problem 1	16			
3	Complete Truth Tables and K-maps	10			
4	Full Adder Using 2 Half-Adders	10			
5	Carry-Propagate-Generate Circuit	8			
6	Full Subtractor Bit-Slice	20			
7	Number Conversion	8			
8	Two's Complement	22			
9	4 Bit Binary Number to 2's Comp.	10			
10	Add and Sub Overflow	8			
11	Multiplier	16			