PROJECT 2: BASIC DIGITAL FUNCTIONS

Digital Logic Project Submission Form Revision Date: Sept 28, 2018 RealDigital

I am submitting my own work, and I accept that penalties will be assessed against me if I submit work that isn't mine.

Print Name _____ Sign Name _____ Date _____

Point Scale
4: Exemplary
3: Complete
2: Incomplete
1: Minor effort
0: Not submitted

#	Deliverable	Wt	Pts	Date	Asst. Signature
	Requirements				
	1: Verify the Result on Blackboard				
1	Program demo	2			
2	Verbal questions answered well	2			
	2: Try POS Instead				
1	Program demo	2			
2	Verbal questions answered well	2			
	3: Circuit IV				
1	Program demo	2			
2	Verbal questions answered well	2			
	4: Create a New Circuit				
1	Program demo	3			
2	Verbal questions answered well	3			
	Challenges				
	1: LED Controller Using Switches				
1	Program demo	1			
2	Verbal questions answered well	1			
	Extensions				
	Describe				
1	Program demo				
2	Verbal questions answered well				
	Homework Problems				
1	Complete Truth Tables	20			
2	Non-Minimized SOP and POS Circuits	8			
3	Truth Tables and Minterm Equations	20			
4	Number of Transistors and Gate name	12			
5	Truth Table based on Verilog Code	10			
6	Sketch Circuits and Write Verilog Code	12			
7	Circle Columns and label Logic Gates	20			
8	Complete Truth Tables for Circuits	12			
9	Total Number of Transistors	18			
10	Simplify Using Boolean Algebra	10			
11	Timing Diagrams	12			