

## 2

## Digital Systems Project #2: Custom IP block for ZYNQ

Submission form revision September 5, 2025

RealDigital  
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I am submitting my own work, and I accept that penalties will be assessed against me if I submit work that isn't mine.

## Point Scale

4: Exemplary  
 3: Complete  
 2: Incomplete  
 1: Minor effort  
 0: Not submitted

Print Name

Sign Name

Date

Asst. Signature

## Circle one column below and modify earned scores accordingly

Weeks Early / Late	-2	-1	0	1	2	3	4	5+
Requirements Points	+14	+7	0	-7	-15	-20	-25	-30
Challenges Points	+8	+4	0	-4	-9	-12	-17	-18

Requirements	Wt	Pt	Score	Possible	Earned
1. Verify the demo project	2			84	
2. Make an 8-channel controller	3				
3. Add an enable	2				
4. Write C functions	3				
5. Control the 7seg display	3				
6. Create an AXI-connected controller	3				
7. Display numerical data	5				
				Modify earned scores by submission week according to table above.	
Challenges				Possible	Earned
1. 7seg hexadecimal memory-mapped controller	2			48	
2. Add Hex and Raw modes	2				
3. Add a BCD decoder	3				
4. C Connect the XADC	5				