## PROJECT 2: CUSTOM IP BLOCK FOR ZYNQ



Revision: 9/3/19

**Digital Systems Project Submission Form** 

I am submitting my own work, and I accept that penalties will be assessed against me if I submit work that isn't mine.						Point Scale 4: Exemplary 3: Complete 2: Incomplete
	Print Name Sign Name				Date	1: Minor effort 0: Not submitted
#	Deliverable	Wt	Pts	Date	Asst. S	ignature
Requirements						
1: Verify the Demo Project						
	Program demo	2				
	Verbal questions answered well	2				
2: Test various blinking patterns						
	Program demo	3				
	Verbal questions answered well	3				
3: Create functions to control LEDs						
	Program demo	3				
	Verbal questions answered well	3				
4:Add an enable function						
	Program demo	3				
	Verbal questions answered well	3				
Challenges						
1:Create IP for RGB LEDs						
	Program demo	1				
	Verbal questions answered well	1				
Extensions						
Describe						
	Program demo					
	Verbal questions answered well					
Homework Problems						
1	Simple handshake system	6				
2	Debouncer	10				
3	States over time	12				
4	State diagram rules	4				
5	When to synchronize outputs	7				
6	Verilog to schematic	10				
7	Counters	10				